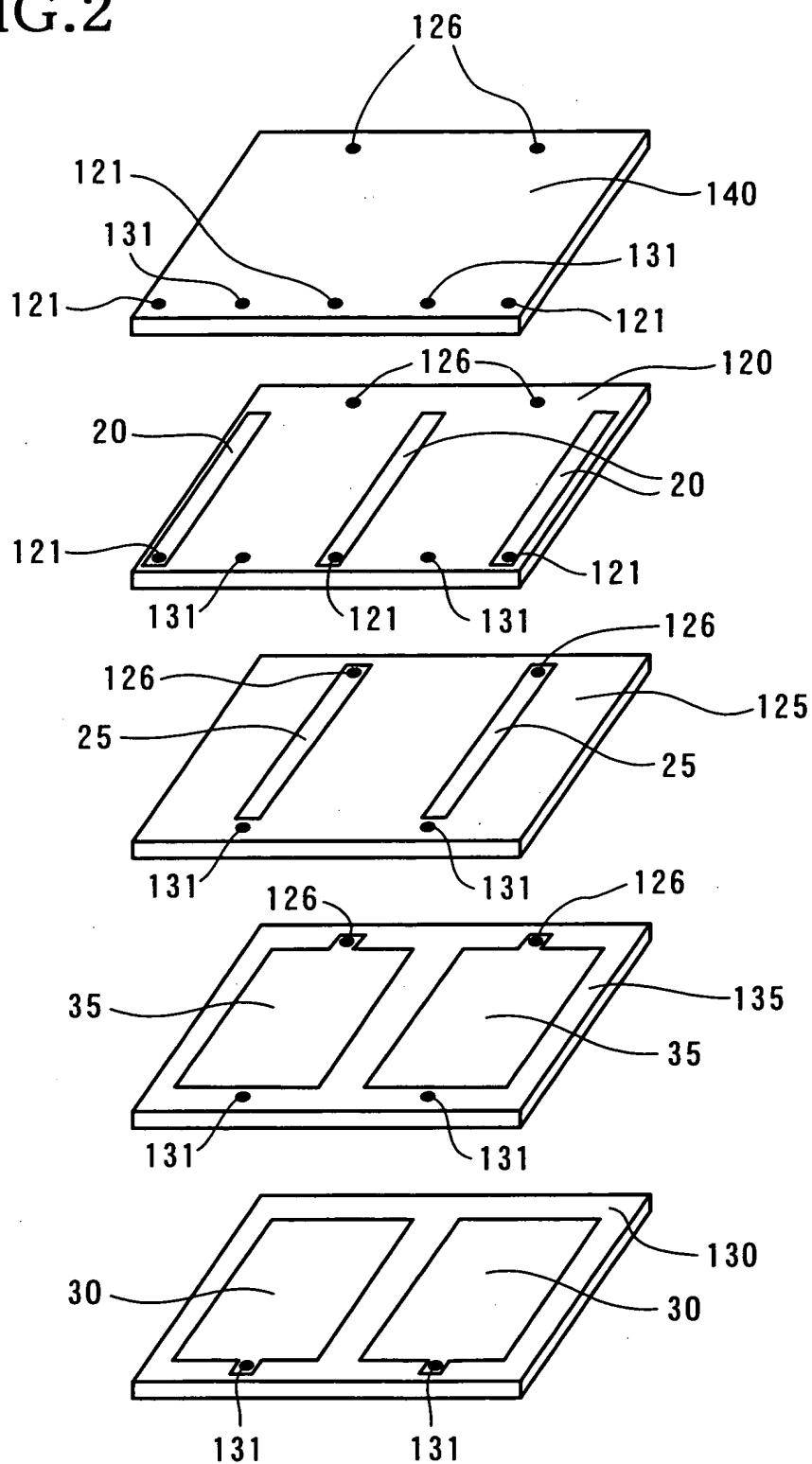


FIG.2



10075452.021502

FIG.3

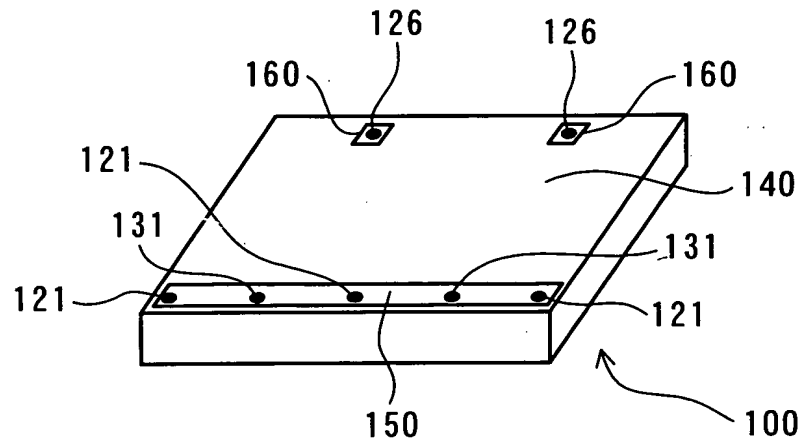


FIG. 4

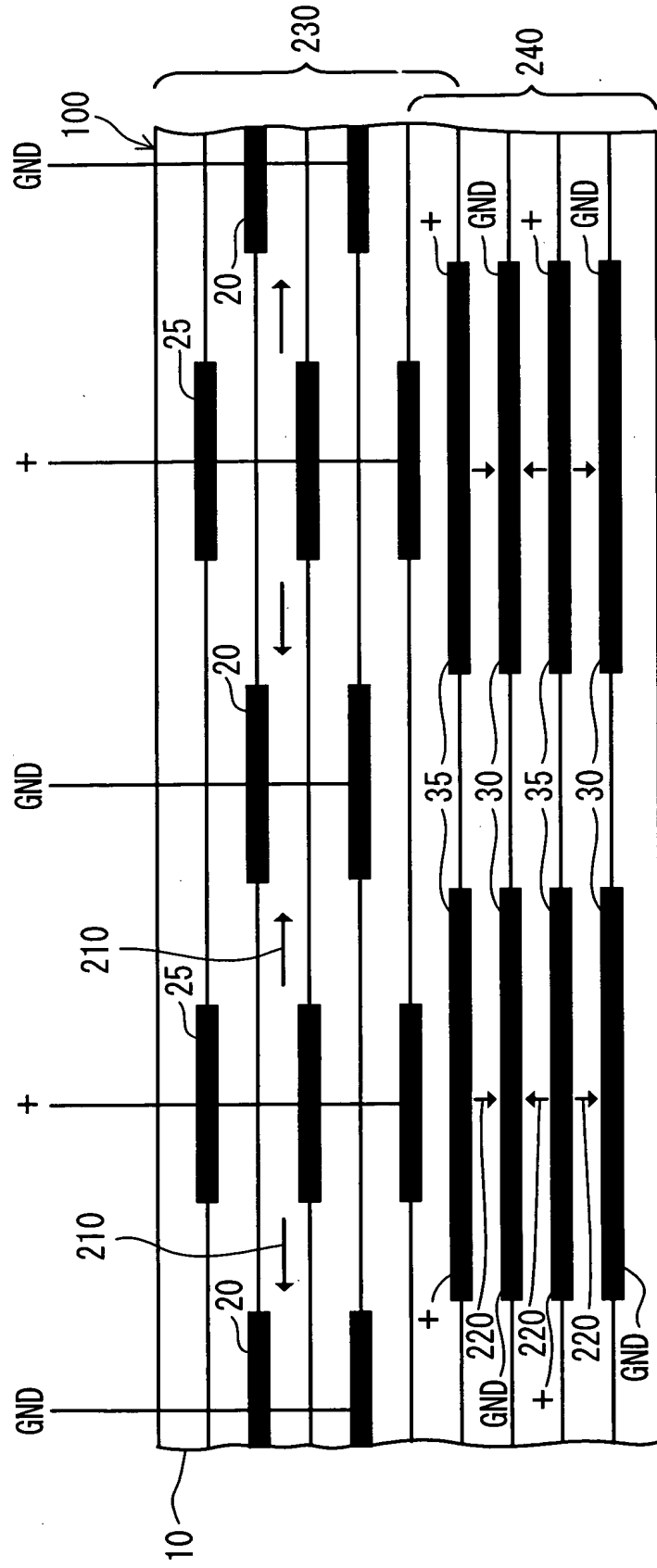


Figure 5 is a plan view and cross-sectional view of a semiconductor device. The plan view (top) shows a grid of conductive lines (10, 20, 20a, 210, 220, 230, 240, 25, 25a, 30, 35, 35a) and a switch (40) connected to a GND line. The cross-sectional view (bottom) shows the layered structure of the device, including a substrate (60), a gate layer (50a), a gate insulator (70), and a conductive layer (90). The device is divided into regions 80 and 80a, with a central region 51.

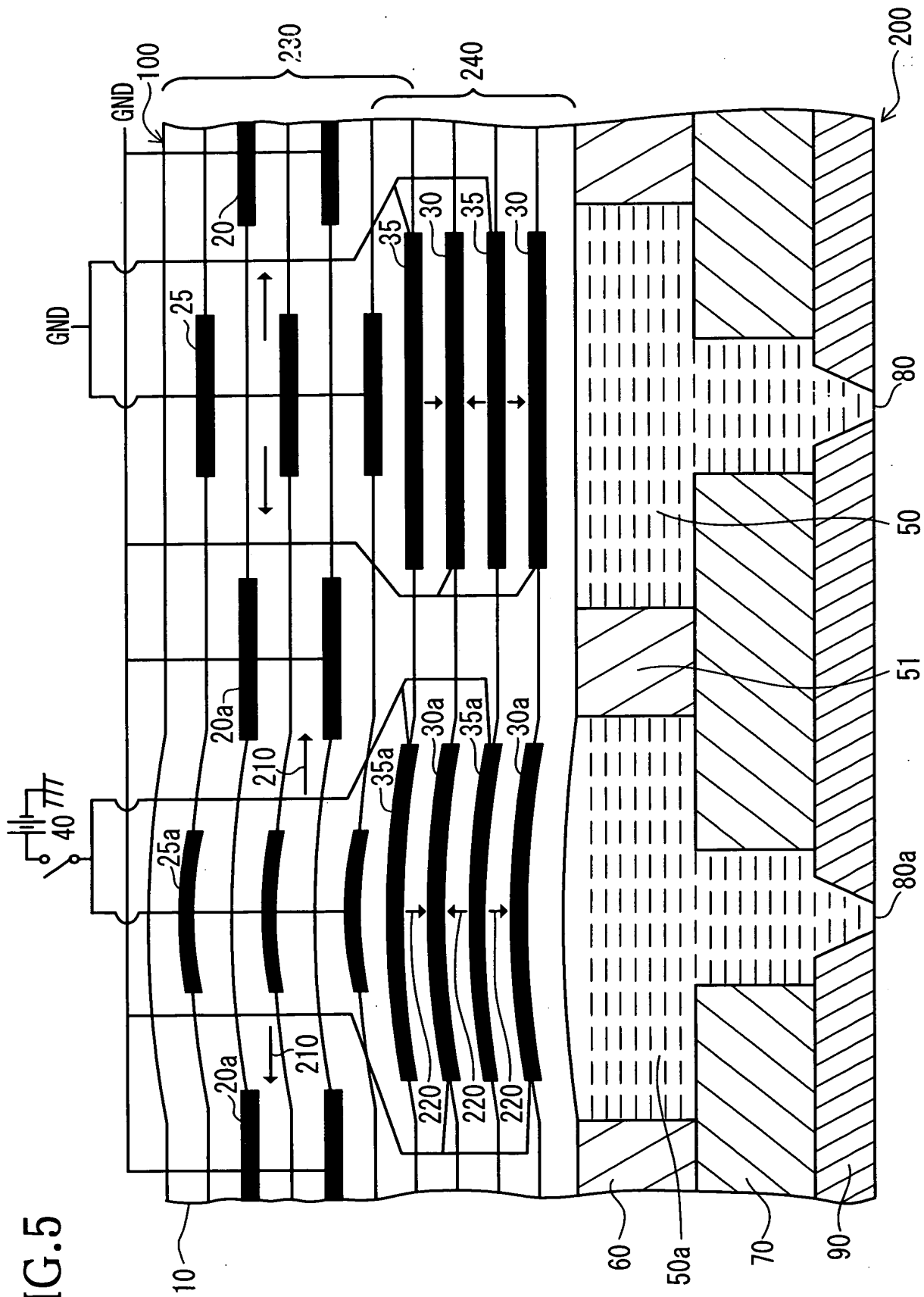




FIG.7

RELATED ART

